







The new adaptive algorithmic advances also make High-Sigma Verifier ideal for large scale deployments. It is very easy to use and does the right thing automatically, which makes it readily deployable to large numbers of engineers with very little training or support. It is also excellent for automated verification of large batches of standard cells, memory IP, and timing arc verification, as it requires no scripting or user intervention to handle corner cases. High-Sigma Verifier is included with Mentor's [Solido Variation Designer](#).



Jeff Dyck is Director of Engineering at Mentor, a Siemens Business.

*Reprinted with permission from Semiconductor Engineering*