

# Eldo Platform

## Advanced verification for analog-centric ICs

### Benefits and Features

#### Comprehensive circuit analysis & diagnostics

- Optimized performance for BCD & high-voltage processes
- Comprehensive analog & RF analyses
- Accurate & fast device noise analysis
- Advanced user-defined static & dynamic SOA checks
- Advanced Analog Characterization

#### Benefits comprehensive circuit analysis & diagnostics

- Incremental, dynamically controlled Monte Carlo convergence
- Monte Carlo acceleration for normal & rare events
- Standard cell & IO characterization with Kronos
- Native multi-threading, distributed processing

#### Industry-Proven reliability verification Solution

- User-defined aging solution with statistical aging & aging sensitivity
- Single-kernel electro-thermal analysis

### Summary

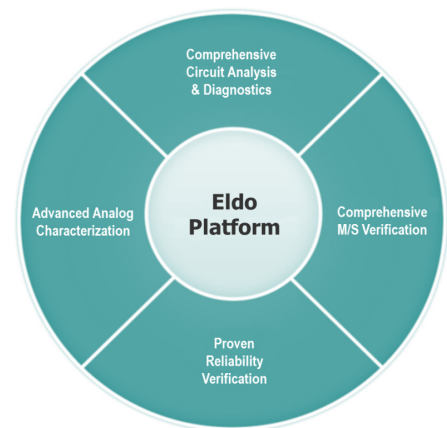
#### A new generation of IC circuit simulation - Eldo Platform

Eldo® Platform offers a complete solution for verifying analog, RF, and mixedsignal circuits for the automotive, industrial, medical, and other mission-critical markets. More than 200 companies rely on the Eldo Platform for safety, reliability, and quality verification of ICs used in application-specific and power management applications.

Eldo Platform delivers the required SPICE accuracy and performance to design and verify complex automotive IC designs using the BCD technology and to address their design and verification challenges efficiently and in a timely manner.

Reliability verification is made possible with Eldo's industry-proven advanced aging simulations to predict reliability issues due to circuit degradation, electrothermal simulation to analyze thermal impact due to power dissipation, and safe operating area simulation and analysis to detect dangerous operating condition violations over the lifetime of the IC.

Certified by the major foundries, Eldo's extensive device model libraries and accuracy make it the simulator of choice for CPU-intensive applications such as digital cell characterization. For silicon-accurate characterization and better yield estimation, advanced statistical verification in Eldo provides smart, incremental analyses with acceleration techniques.



Eldo Platform is the industry proven, most advanced circuit verification platform for analogcentric circuits, offering a differentiated solution for reliability verification and comprehensive circuit analysis and diagnostics for analog, RF, and mixed-signal circuits.

# Eldo Platform

## Benefits *continued*

- Analog defect coverage for AMS ICs

## Comprehensive mixed-signal verification with Questa ADMS

- Full HDL/HDL-AMS language, UVM, UPF, & Matlab® support
- Unified, coverage-driven mixed-signal verification
- Flexible, easy-to-use design reconfiguration

Incorporated in Questa® ADMS, Eldo offers complete support for SPICE and all standard HDL and HDL-AMS languages to cover mixed-signal needs using Questa technology. The solution also offers a common verification platform for analog, digital, and mixed-signal flows with unified coverage-driven, flexible, and easy-to-use design reconfiguration for mixed-signal verification. Eldo Platform employs three engines:

- Eldo Classic offers proven sign-off accuracy to design complex automotive IC designs using the BCD technology with a comprehensive set of circuit analysis and debugging tools
- Eldo Premier is an accelerated SPICE-accurate, analog-centric circuit simulator that offers improved performance for mission-critical applications using BCD technology
- Eldo RF is a high-performance RF IC verification simulator

## Eldo Platform functionality

### Comprehensive circuit analysis & diagnostics

- Accurate, fast device noise analysis in time & frequency domains
- Transient, DC, & AC sensitivity analyses
- Multi-tone harmonic balance & shooting analyses
- Real-time design tuning
- General-purpose optimizer
- Comprehensive post-processing
- Power consumption analysis
- Circuit & model profiling
- Comprehensive waveform analysis
- Advanced results browsing & filtering

### Comprehensive mixed-signal verification with Questa ADMS

- Common verification for analog, digital, and mixed-signal flows
- Full support for VHDL, Verilog, SystemVerilog, VHDL-AMS, Verilog-AMS, SystemC, UVM, UPF, & Matlab
- Unified, coverage-driven verification
- Flexible, easy-to-use design reconfiguration

**Advanced analog characterization**

- Incremental, dynamically controlled Monte Carlo convergences
- Monte Carlo acceleration for normal & rare events
- Standard cell and IO characterization with Kronos (NLDM/CCS/ECSM, Verilog, VITAL)
- Native multi-threading with distributed processing with dispatcher support

**Industry-Proven reliability verification**

- Customizable models for aging, statistical aging, & aging sensitivity simulations
- Single-kernel electro-thermal analysis solving the full electro-thermal system
- Advanced, user-defined static and dynamic SOA checks
- Analog defect coverage for AMS ICs with Tessent® DefectSim

**Eldo Platform specifications****input/output**

- SPICE formats: Eldo, HSPICE, Spectre®
- Parasitics: DSPF, SPEF back annotation
- Digital inputs: VCD, data, vectors
- Output formats: WDB, FSDB, tr0, PSF, Touchstone/CIT1

**Model support**

- BSIM3, BSIM4, PSP, BSIMCMG, BSIMSOI
- HiSIM, HiSIM/HV, EKV, HICUM, VBIC, MEXTRAM
- MOS1/3/11, BJT, MESFET, JFET, Diode
- S/Y/Z-parameter, Microstrip, Transmission Lines, IBIS, Verilog-A

**Design flow integration**

- design environments: Pyxis® Schematic and Tanner S-Edit
- Cadence® Virtuoso® ADE

**Hardware requirements**

- Single or multi-core systems with LSF/OGE/RTDA support, 64-Linux®
- Minimum 5 GB of disk space, 256 MB physical memory, 512 MB swap space

Siemens Digital Industries Software  
siemens.com/software

Americas +1 314 264 8499  
Europe +44 (0) 1276 413200  
Asia-Pacific +852 2230 3333