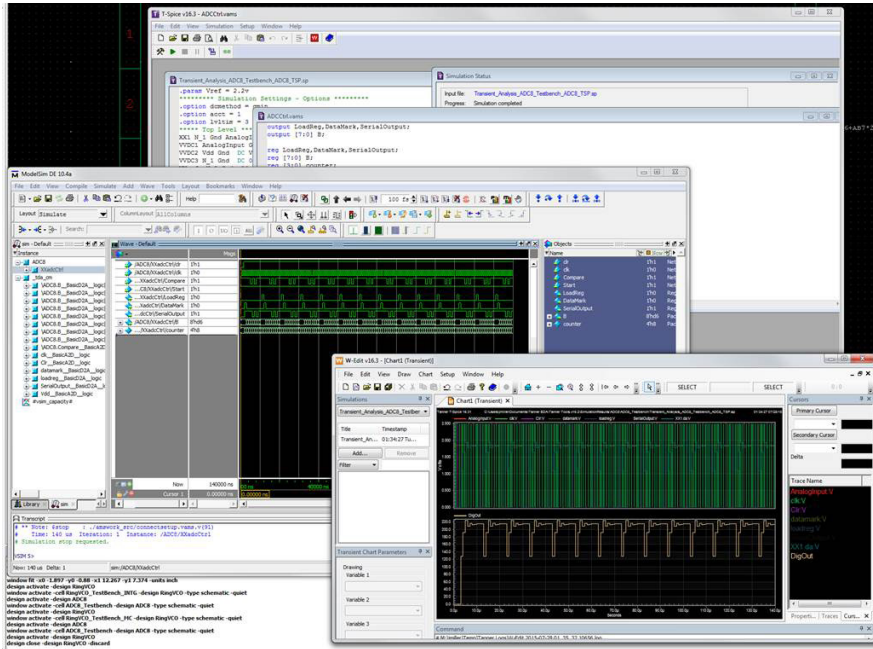


Tanner T-Spice AMS Simulation



Tanner T-Spice Simulator UI, with schematic, spice netlist, simulation logs and waveform data.

Seamless AMS design and simulation solution

Tanner T-Spice AMS Simulation is a complete analog and digital IC simulation environment in one highly-integrated end-to-end flow. Engineers can perform top-down co-simulation, AMS simulation or advanced debugging from within one unified flow. The tool brings together the high-performance simulators T-Spice, Questa or ModelSim and an advanced debugging environment.

Tanner S-Edit Capture
schematic and HDL capture

Tanner T-Spice Simulator
analog simulation

Tanner T-Spice AMS
co-simulation

Questa® / ModelSim®
digital simulation

Tanner Waveform Viewer
waveform analysis

Complete analog and digital IC simulation environment.

FEATURES AND BENEFITS:

- High-performance mixed-language simulation
- Top-down mixed-signal co-simulation
- Simulate combined netlists at various abstraction levels: behavioral models, block-level RTL, and gate- and transistor-level blocks
- Debugging environment: mixed-language, IDE, code tracing and waveform
- Standards-based support for Verilog, Verilog-A and Verilog-AMS
- SDF support
- Intuitive and easy to use; quick learning curve
- Unparalleled customer support
- Flexible licensing

AMS design from the top down

Tanner T-Spice AMS Simulation enables abstract top-down design. The package allows designers to start from the top level with abstract Verilog AMS, fill in detailed block-level RTL, then complete gate- and transistor-level detail. This stepwise approach leads to a more predictable design cycle and allows teams to catch integration issues early while they're still easy to fix. The abstract top-level modules become an executable specification, reducing ambiguity and improving communication between the analog and digital design teams.

High performance AMS simulation

Tanner T-Spice Simulation offers a complete mixed-signal IC design front end. Create AMS designs in Tanner S-Edit, then directly invoke analog, digital or mixed-signal simulations in T-Spice, Questa or ModelSim with one click. Debug using the Tanner Waveform Viewer analog waveform analysis platform and the powerful debug environment in Questa or ModelSim. Co-simulation combines the high speed of event-driven digital simulation for the digital portions of the design with detailed continuous-time analog modeling in the SPICE engine for maximum mixed signal performance. Highly configurable connect modules converting between logic and analog domains are automatically inserted where needed with no required user intervention. The result: seamless co-simulation without setup hassles.

For the latest product information, contact us at: www.mentor.com, (800) 547-3000

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